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STUDY MODULE DESCRIPTION FORM								
Name of the module/subject  Electronics					Coc <b>10</b> 1	le 10331131010330033		
Field of study				Profile of study		Year /Semester		
Cont	rol Engineering	and Robotics		(general academic, practical) (brak)		2/3		
	path/specialty			Subject offered in:		Course (compulsory, elective)		
		•	1	polish obligatory				
Cycle of	study:		For	Form of study (full-time,part-time)				
	First-cyc	cle studies		full-time				
No. of h	ours					No. of credits		
Lectur	e: 2 Classes	s: - Laboratory: 2		Project/seminars:	-	5		
Status o	=	program (Basic, major, other)	(	university-wide, from another fi	,	-1-1		
		(brak)			(bra	•		
Education	on areas and fields of sci	ence and art				ECTS distribution (number and %)		
techr	ical sciences					5 100%		
Responsible for subject / lecturer:  dr inż. Jan Deskur email: Jan.Deskur@put.poznan.pl tel. +48 61 665 2735 Wydział Elektryczny ul. Piotrowo 3A 60-965 Poznań								
Prere	quisites in term	s of knowledge, skills an	d s	ocial competencies:				
1	Knowledge	K_W02:						
2	Skills	K_U01: K_U04:						
3	Social competencies	K_K_02:						
Assu	mptions and obj	ectives of the course:						
Knowle	edge concerning princ	iples of operation of the electronic	circ	uits;				
the abi	lity of analysis as well	as designing the electronic circuit	s.					
	Study outco	mes and reference to the	ed	ucational results for	a f	ield of study		
Knowledge:								
	/_12 - [K_W12]							
Skills	:							
1. K_U15 - [K_U15]								
2. K_U20 - [K_U20]								
3. K_U23 - [K_U23]								
Social competencies:								
1. K_K04 - [K_K04]								
Assessment methods of study outcomes								
- Lectu	res: written test , exar	nination in semester 4						
- Labor	atory: attendance in e	exercises, evaluation of written rep	orts	on laboratory exercises.				
Course description								

# **Faculty of Electrical Engineering**

- Lectures: Passive electronic components. Semiconductor materials; p-n junction. Diodes, its models and applications; Bipolar transistors, field effect transistors. Integrated circuits of small and medium scale of integration. Operational amplifiers. Applications of operational amplifiers to analogue signal processing. Analogue controllers and filters. Electronic switches, S&H, DA and AD converters; switched capacitors devices. Selected problems of industrial electronics.

Laboratory: diodes, transistors, operational amplifiers, filters; circuit-oriented simulation programs.

## Basic bibliography:

- 1. Lecture materials provided by the teacher in electronic form
- 2. Elektronika. Układy elektroniczne, Waldemar Nawrocki, WPP, Poznań 2010
- 3. Wprowadzenie do elektroniki i energoelektroniki, Marian P. Kaźmierkowski, Jerzy T. Matysik, Oficyna Wyd. Pol. Warszawskiej, Warszawa, 2005

### Additional bibliography:

- 1. Układy półprzewodnikowe, Ulrich Tietze, Christoph Schenk, WNT, Warszawa, 1996,2009
- 2. Elementy i układy elektroniczne w pytaniach i odpowiedziach, Mirosław Rusek, Jerzy Pasierbiński, WNT, Warszawa, 2006

### Result of average student's workload

Activity	Time (working hours)
1. Lectures	30
2. Laboratory excersises	30
3. Preparation to laboratory excersises, elaboration of reports	30
4. Home excersises	15
5. Preparation to test	15
6. Attendance in consultations	5

#### Student's workload

Source of workload	hours	ECTS
Total workload	125	5
Contact hours	65	3
Practical activities	60	2